



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani et al      Art Unit : 1765  
 Serial No. : 08/690,747      Examiner : Robert Kunemund  
 Filed : August 1, 1996  
 Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

COPY OF PAPERS  
ORIGINALLY FILED

Commissioner for Patents  
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.

The required fees are computed below. Please apply any charges  
not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	62	-	62	=	0	\$0
Independent	13	-	13	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0
Applicant hereby petitions under 37 C.F.R. \$1.136 for a 2 month extension of time						\$400
TOTAL FEE DUE						\$400

A check for \$400 is attached.

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I hereby certify under 37 CFR \$1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Applicant: Hisashi Ohtani et al  
Serial No.: 08/690,747  
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Page: 2

Attorney's Docket No. 07977-052001 / US3053

Respectfully submitted,

*Donald C. Kordach*  
Reg. No. 38,213

Date: \_\_\_\_\_

*10/17/2001*

*for*

\_\_\_\_\_  
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